



MCSO1EU
Application Manual

Application Manual

MCSO1EU

**Ultra Low Power
High Temperature
Clock Oscillator
32.768 kHz**

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MCSO1EU

Ultra Low Power High Temperature Clock Oscillator 32.768 kHz

1. OVERVIEW

- Oscillator with built-in AT-cut crystal, operating in fundamental mode
- Overall frequency stability over temperature range:
 - $\leq \pm 100$ ppm at C = -55 to +125°C
 - $\leq \pm 150$ ppm at E = -55 to +150°C
 - $\leq \pm 300$ ppm at D = -55 to +175°C
- Frequency stability guaranteed for 1000 h at T_{MAX}
- Very fast start-up: typ.1 ms at 25°C
- Output signal HCMOS compatible
- High shock and vibration resistance
- Operating voltage: 2.5 V or 3.3 V
- Power consumption:
 - < 20 µA at V_{DD} = 2.5 V and 3.3 V
- Operating temperature range:
 - C = -55 to +125°C
 - E = -55 to +150°C
 - D = -55 to +175°C
 - X = Custom
- Enable/Disable function (option 1):
 - Standby current I_{DDD} < 2 µA
- Low aging rate
- Ceramic SMD package (8.0 x 3.7 mm), RoHS-compliant and 100% lead-free

1.1. GENERAL DESCRIPTION

The MCSO1EU combines an Ultra Low Power, High Temperature, High Frequency HCMOS oscillator circuitry together with an AT-cut quartz crystal in a hermetically sealed ceramic package with metal or ceramic lid. No external components are required.

The frequency output 32.768 kHz on F_{OUT} pin can be enabled / disabled by the E/D pin (option 1). The F_{OUT} frequency is enabled when E/D pin is connected to V_{DD} or when it is left open (not connected). When the E/D pin is tied to GND the frequency is disabled and the F_{OUT} pin is in high impedance state (Hi-Z).

1.2. APPLICATIONS

The MCSO1EU clock oscillator module combines outstanding performance and robustness in a standard ceramic package:

- Oscillator module (embedded XTAL) in a standard 8.0 x 3.7 mm lead-free ceramic package
- Ultra Low Power
- Temperature range up to +175°C
- High shock and vibration resistant

The robust construction and high performances make this product perfectly suitable for many high reliability applications:

- Harsh Environment: Geothermal Equipment, Down Hole and Well Drilling
- Avionics / Aerospace: Airborne Equipment / Engine Control / Actuator Controller / Temperature Controller / Instrumentation / Navigation and Guidance Systems
- Instrumentation: Field Test Instrument / Automotive Test Equipment
- Security / Safety: Survival and Protection System / Railway Security System / Fire Fighter Equipment
- Industrial: Telemetry / A/C Controller / Hydraulic Sensor

1.3. ORDERING INFORMATION

Example: MCSO1EU C V – D 32.768 kHz E/D T3 XXX

Code	Package Size
MCSO1EU	8.0 x 3.7 mm

Code	Lid
C	Ceramic lid
Blank	Kovar lid

Code	Supply voltage
W	V _{DD} = 2.5 V
V	V _{DD} = 3.3 V

Code	Temperature range
C	-55 to +125°C
E	-55 to +150°C
D	-55 to +175°C
X	Custom

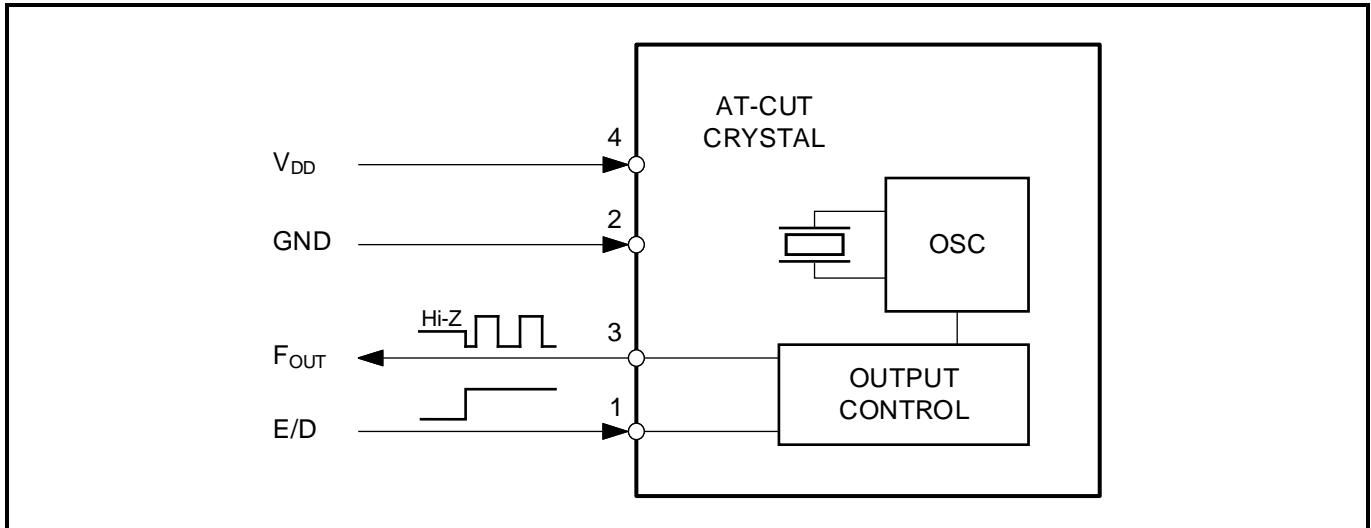
Code	Frequency
32.768 kHz	32.768 kHz

Code	Option 1
E/D	Enable/Disable
Blank	No function

Code	Option 2
T3	SnAgCu solder dipped pads
Blank	Au flashed pads

Code	Customer specification N°
XXX	XXX

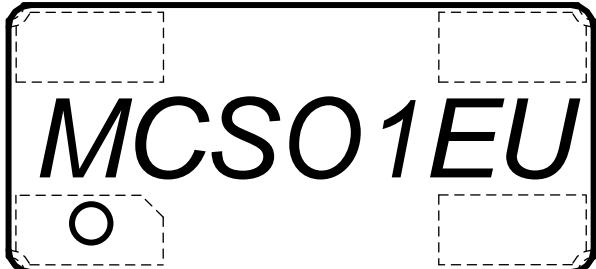
2. BLOCK DIAGRAM



2.1. PINOUT

MCSO1EU Package: (top view)

#4



#3

#1	E/D
#2	GND
#3	F_{OUT}
#4	V_{DD}

#1

#2

2.2. PIN DESCRIPTION

Symbol	Pin #	Description
E/D	1	Input to enable/disable the F_{OUT} pin (option 1). If E/D is HIGH or floating (V_{IH} or open), the F_{OUT} pin is in output mode. When E/D is tied to Ground (V_{IL}), the F_{OUT} pin is disabled (Hi-Z).
GND	2	Ground.
F_{OUT}	3	Clock Output; three-state; controlled by E/D. If E/D is HIGH (V_{DD}) or floating, the F_{OUT} pin drives the square wave of the frequency. When E/D is tied to Ground, the F_{OUT} pin is disabled (Hi-Z).
V_{DD}	4	Power Supply Voltage.

3. ELECTRICAL SPECIFICATIONS

3.1. ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings according to IEC 60134:

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{DD}	Power supply voltage	Nominal $V_{DD} = 2.5 \text{ V}$	-0.3	3	V
		Nominal $V_{DD} = 3.3 \text{ V}$	-0.3	3.8	
V_I	Input voltage		-0.3	V_{DD}	V
V_O	Output voltage		-0.3	V_{DD}	V
V_{ESD}	ESD voltage	HBM (1)		± 2000	V
		MM (2)		± 200	
T_C	Operating temperature range C		-55	125	°C
T_E	Operating temperature range E		-55	150	°C
T_D	Operating temperature range D		-55	175	°C
T_{STO}	Storage temperature	Stored as bare product	-65	125	°C
T_{PEAK}	Maximum reflow condition	JEDEC J-STD-020C		265	°C

(1) HBM: Human Body Model, according to JESD22-A114.
(2) MM: Machine Model, according to JESD22-A115.

3.2. OPERATING PARAMETERS

For this Table, V_{DD} = nominal voltage; GND = 0 V; T_A = 25 °C; unless otherwise indicated.

Operating Parameters:

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
Supply						
V_{DD}	Power supply voltage (1)	Nominal V_{DD} = 2.5 V	2.375		2.625	V
		Nominal V_{DD} = 3.3 V	3.135		3.465	
I_{DD}	Input current. V_{DD} = 2.5 V F_{OUT} enabled, C_L = 10 pF	32.768 kHz			20	μA
	Input current. V_{DD} = 3.3 V F_{OUT} enabled, C_L = 10 pF	32.768 kHz			20	
I_{DDD}	Standby current. Input current, output disabled	E/D ≤ 0.3 V			2	μA
Input E/D						
V_I	Input voltage		GND -0.3		V_{DD} +0.3	V
V_{IL}	LOW level input voltage		GND		0.3 V_{DD}	V
V_{IH}	HIGH level input voltage		0.7 V_{DD}		V_{DD}	V
t	Reaction time				5	ms
Output F_{OUT} (HCMOS compatible)						
V_{OH}	HIGH level output voltage		V_{DD} -0.5			V
V_{OL}	LOW level output voltage				0.4	V
C_L	Output load capacitance, HCMOS		3	15	27	pF
t_r / t_f	Rise & fall time	C_L = 15 pF, 20% to 80% V_{DD}			25	ns

(1) A 47 nF decoupling capacitor has to be connected between V_{DD} and GND.

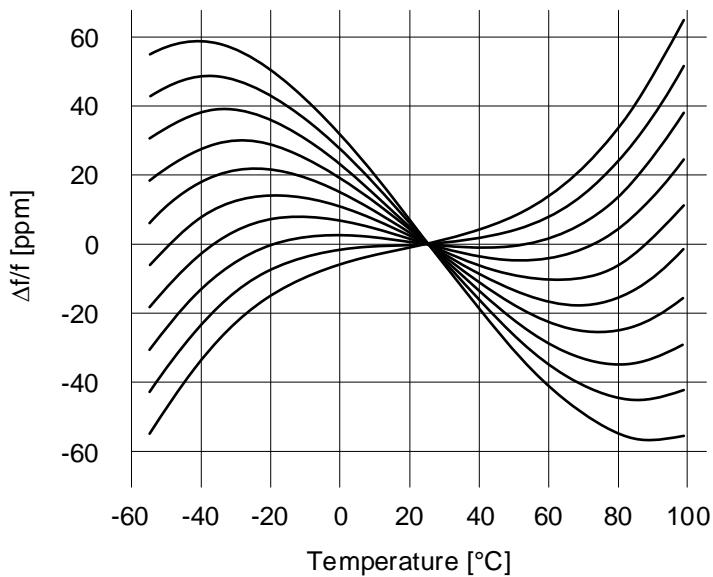
3.3. OSCILLATOR PARAMETERS

For this Table, V_{DD} = nominal voltage; GND = 0 V; T_A = 25 °C; unless otherwise indicated.

Oscillator Parameters:

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
General						
F	Frequency			32.768		kHz
t_{START}	Oscillator start-up time			1	5	ms
δ_{FOUT}	F_{OUT} duty cycle @ $V_{DD}/2$		40		60	%
Frequency Characteristics						
$\Delta F/F$	Overall frequency stability over temperature range (see ORDERING INFORMATION)	Including adjustment at +25°C, long term aging 1000 h at T_{MAX} , V_{DD} variations ±5% and C_L variations min. to max. C = -55 to +125°C E = -55 to +150°C D = -55 to +175°C				ppm
				±100	±150	±300

3.3.1. FREQUENCY VS. TEMPERATURE CHARACTERISTICS



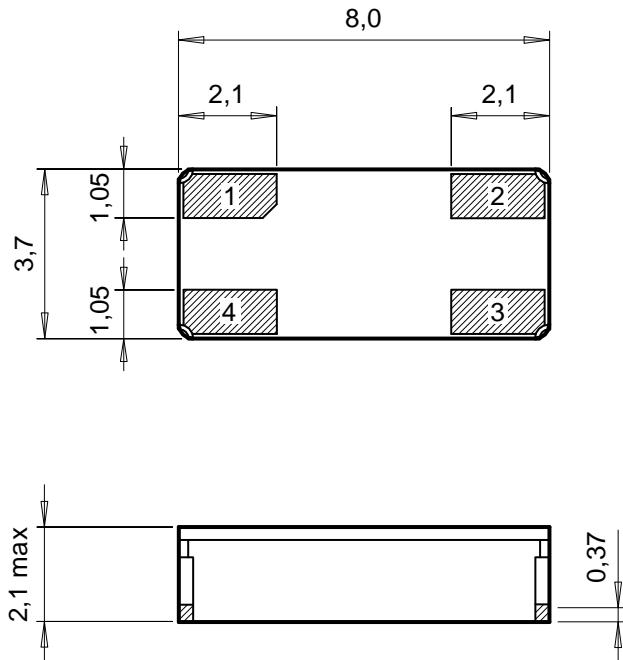
4. PACKAGE

4.1. DIMENSIONS AND SOLDER PAD LAYOUT

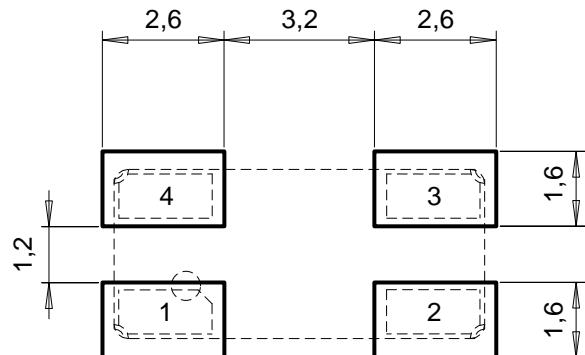
4.1.1. PACKAGE WITH METAL LID

MCSO1EU Package with Metal lid (Blank):

Package dimensions (bottom view):



Recommended solder pad layout:



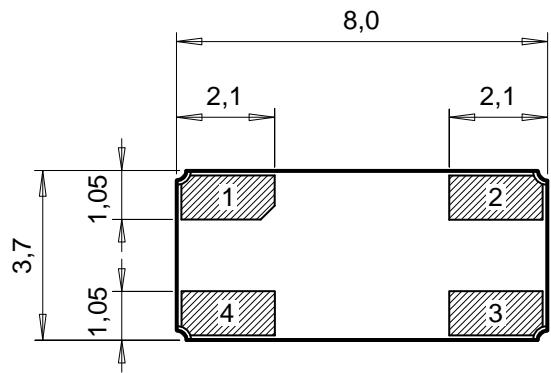
Tolerances: unless otherwise specified ± 0.1 mm
Drawing: MCSO1EU_Metal_Pack-drw_20210709

All dimensions in mm typical.

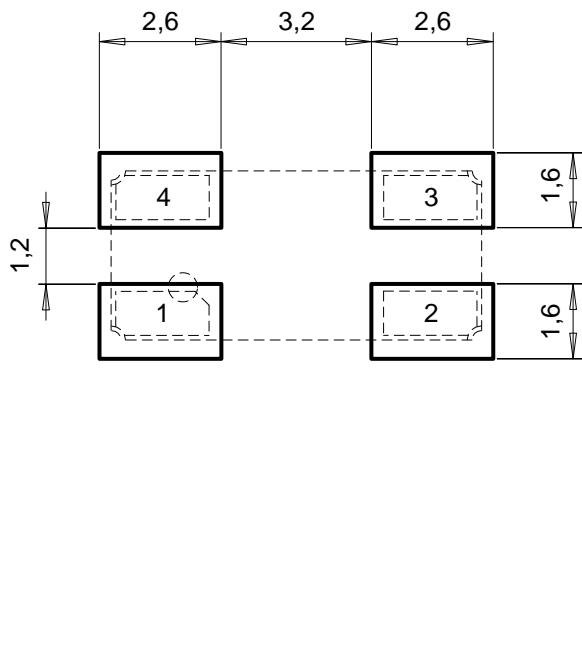
4.1.2. PACKAGE WITH CERAMIC LID

MCSO1EU Package with Ceramic lid (C):

Package dimensions (bottom view):



Recommended solder pad layout:

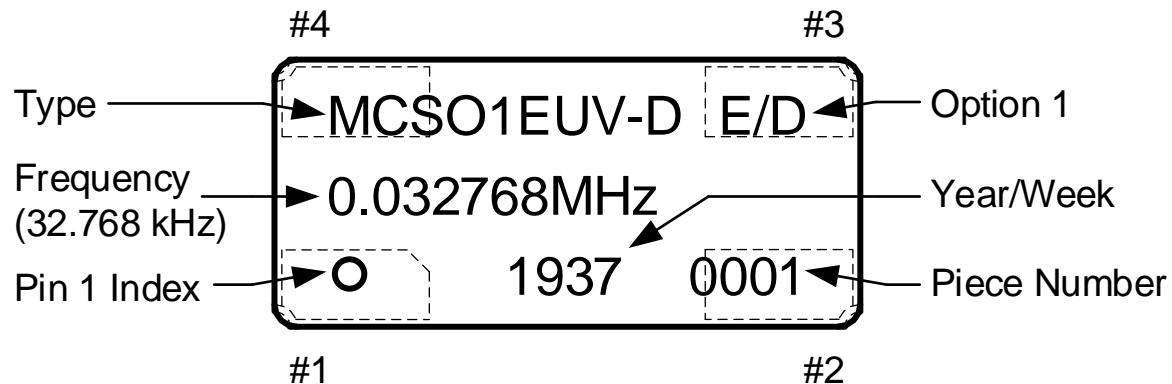


Tolerances: unless otherwise specified ± 0.1 mm
Drawing: MCSO1EU_Ceramic_Pack-drw_20210709

All dimensions in mm typical.

4.2. MARKING AND PIN #1 INDEX

Laser marking MCSO1EU Package: (top view)

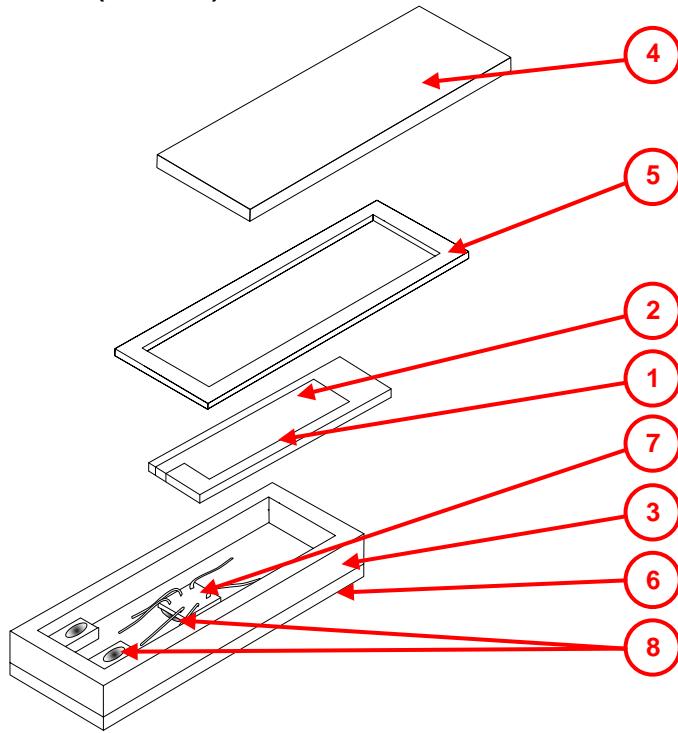


5. MATERIAL COMPOSITION DECLARATION & ENVIRONMENTAL INFORMATION

5.1. HOMOGENOUS MATERIAL COMPOSITION DECLARATION (METAL LID)

Homogenous material information according to IPC-1752 standard

Material Composition MCSO1EU (Metal lid):



(Symbolic drawing)



No.	Item Component Name	Sub Item Material Name	Material Weight (mg)	Weight (%)	Substance Element	CAS Number	Comment
1	Resonator	Quartz Crystal	4.8	100%	SiO ₂	14808-60-7	
2	Electrodes	Cr+Au	0.2	5% 95%	Cr Au	7440-47-3 7440-57-5	
3	Housing	Ceramic	114.0	100%	Al ₂ O ₃	1344-28-1	
4	Metal Lid	Kovar Lid Ni-plating Au-plating	59.8	90% 9% 1%	Fe53Ni29Co18 Ni Au	7439-89-6 7440-02-0 7440-48-4 7440-02-0 7440-57-5	Nickel plating Gold plating
5	Seal	Solder Preform	6.5	80% 20%	Au80 / Sn20	7440-57-5 7440-31-5	
6	Terminations	Internal and external terminals	5.3	80% 15% 5%	W Ni Au 0.2 micron	7440-33-7 7440-02-0 7440-57-5	Tungsten Nickel plating Gold plating
7	HCMOS IC	Silicon Die pad plating Bonding wires	4.0	69% 1% 30%	Si Al Au	7440-21-3 7429-90-5 7440-57-5	
8	Conductive adhesive	Silver filled Epoxy	1.4	70% 30%	Ag EP	7440-22-4 129915-35-1	
		Unit weight typ. ±10%	196				

5.2. RECYCLING MATERIAL INFORMATION (METAL LID)

Recycling material information according to IPC-1752 standard.

Element weight is accumulated and referenced to the unit weight of 196 mg.

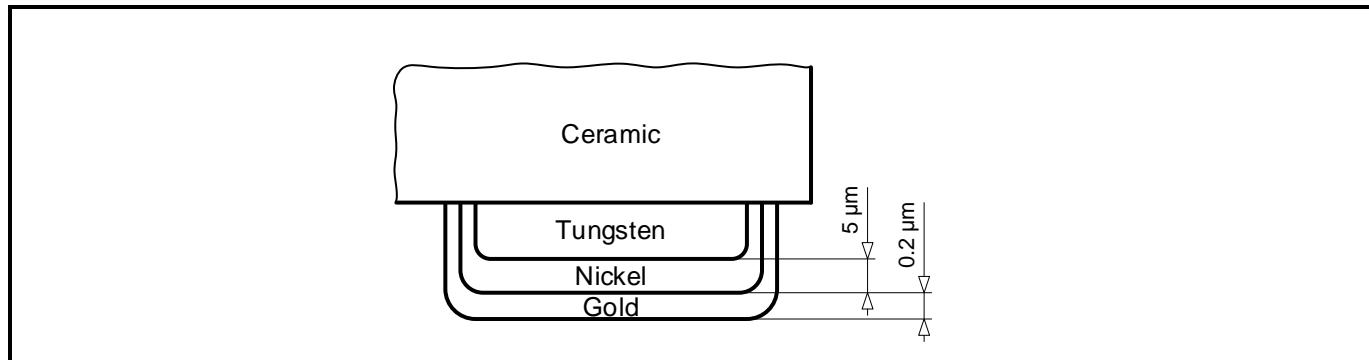
Item Material Name	No.	Item Component Name	Material Weight (mg)	Substance Element (%)	CAS Number	Comment
Quartz Crystal	1	Resonator	4.8	SiO ₂ 2.45	14808-60-7	
Chromium	2	Electrodes	0.01	Cr 0.005	Cr: 7440-47-3	
Ceramic	3	Housing	114.0	Al ₂ O ₃ 58.2	1344-28-1	
Gold	2 4 5 6 7c	Electrodes Metal Lid Seal Terminations HCMOS IC	7.45	Au 3.8	Au: 7440-57-5	
Tin	5	Seal	1.3	Sn 0.66	Sn: 7440-31-5	
Nickel	4 6	Metal Lid (Plating) Terminations	6.18	Ni 3.15	Ni: 7440-02-0	
Tungsten	6	Terminations	4.24	W 2.16	W: 7440-33-7	
Kovar	4	Metal Lid	53.8	Fe53Ni29Co18 27.5	Fe: 7439-89-6 Ni: 7440-02-0 Co: 7440-48-4	
Silicon	7a	HCMOS IC	2.76	Si 1.41	Si: 7440-21-3	
Aluminum	7b	HCMOS IC	0.04	Al 0.02	Al: 7429-90-5	
Silver	8a	Conductive adhesive	0.98	Ag 0.50	Ag: 7440-22-4	
Epoxy	8b	Conductive adhesive	0.42	EP 0.21	129915-35-1	
Unit weight (total) typ. ±10%			196	100		

5.3. ENVIRONMENTAL PROPERTIES & ABSOLUTE MAXIMUM RATINGS (METAL LID)

Package	Description
DFN-4 ceramic package	Dual Flat No Leads (DFN), hermetically sealed ceramic package with metal lid.

Parameter	Directive	Conditions	Value
Product weight (total)			196 mg
Storage temperature		Store as bare product	-65 to +125°C
Moisture sensitivity level (MSL)	IPC/JEDEC J-STD-020D		MSL1
MTTF			> 100 years

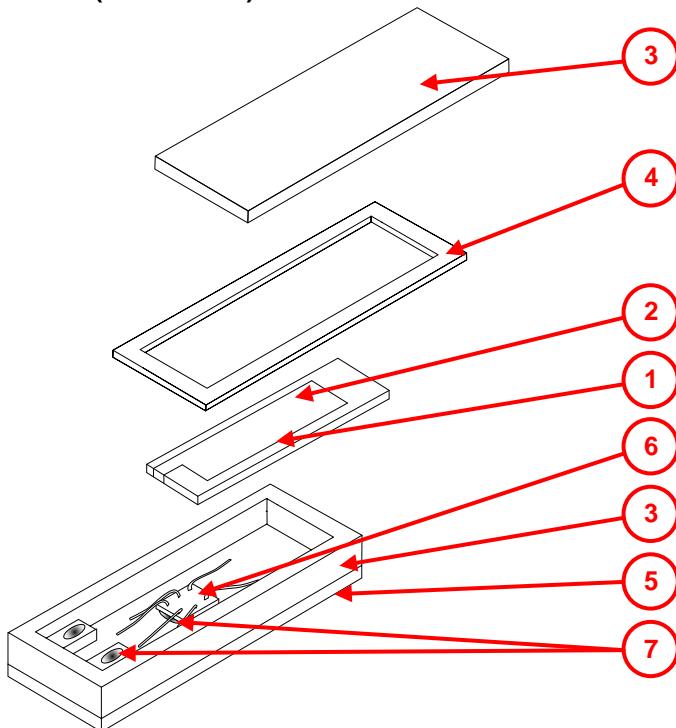
Terminal finish:



5.4. HOMOGENOUS MATERIAL COMPOSITION DECLARATION (CERAMIC LID)

Homogenous material information according to IPC-1752 standard

Material Composition MCSO1EU (Ceramic lid):



(Symbolic drawing)



No.	Item Component Name	Sub Item Material Name	Material Weight (mg)	Material Weight (%)	Substance Element	CAS Number	Comment
1	Resonator	Quartz Crystal	4.8	100%	SiO ₂	14808-60-7	
2	Electrodes	Cr+Au	0.2	5% 95%	Cr Au	Cr: 7440-47-3 Au: 7440-57-5	
3	Housing	Ceramic	171.0	100%	Al ₂ O ₃	1344-28-1	
4	Seal	Solder Preform	6.5	80% 20%	Au80 / Sn20	Au: 7440-57-5 Sn: 7440-31-5	
5	Terminations	Internal and external terminals	7.1	80% 15% 5%	W Ni Au 0.2 micron	W: 7440-33-7 Ni: 7440-02-0 Au: 7440-57-5	Tungsten Nickel plating Gold plating
6	HCMOS IC	Silicon Die pad plating Bonding wires	4.0	69% 1% 30%	Si Al Au	Si: 7440-21-3 Al: 7429-90-5 Au: 7440-57-5	
7	Conductive adhesive	Silver filled Epoxy	1.4	70% 30%	Ag EP	Ag: 7440-22-4 129915-35-1	
		Unit weight typ. ±10%	195				

5.5. RECYCLING MATERIAL INFORMATION (CERAMIC LID)

Recycling material information according to IPC-1752 standard.

Element weight is accumulated and referenced to the unit weight of 195 mg.

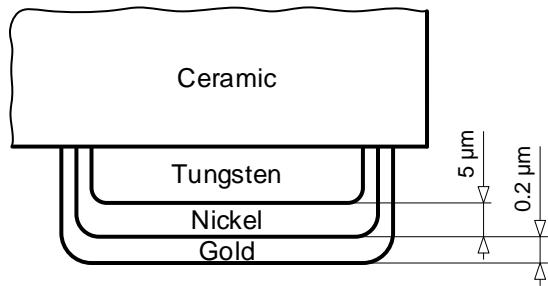
Item Material Name	No.	Item Component Name	Material Weight (mg)	Substance Element (%)	CAS Number	Comment
Quartz Crystal	1	Resonator	4.8	SiO ₂ 2.46	14808-60-7	
Chromium	2	Electrodes	0.01	Cr 0.005	Cr: 7440-47-3	
Ceramic	3	Housing	171.0	Al ₂ O ₃ 87.7	1344-28-1	
Gold	2 4 5 6c	Electrodes Seal Terminations HCMOS IC	6.95	Au 3.56	Au: 7440-57-5	
Tin	4	Seal	1.30	Sn 0.67	Sn: 7440-31-5	
Nickel	5	Terminations	1.07	Ni 0.55	Ni: 7440-02-0	
Tungsten	5	Terminations	5.68	W 2.91	W: 7440-33-7	
Silicon	6a	HCMOS IC	2.76	Si 1.42	Si: 7440-21-3	
Aluminum	6b	HCMOS IC	0.04	Al 0.02	Al: 7429-90-5	
Silver	7a	Conductive adhesive	0.98	Ag 0.50	Ag: 7440-22-4	
Epoxy	7b	Conductive adhesive	0.42	EP 0.22	129915-35-1	
Unit weight (total) typ. ±10%			195	100		

5.6. ENVIRONMENTAL PROPERTIES & ABSOLUTE MAXIMUM RATINGS (CERAMIC LID)

Package	Description
DFN-4 ceramic package	Dual Flat No Leads (DFN), hermetically sealed ceramic package with ceramic lid.

Parameter	Directive	Conditions	Value
Product weight (total)			195 mg
Storage temperature		Store as bare product	-65 to +125°C
Moisture sensitivity level (MSL)	IPC/JEDEC J-STD-020D		MSL1
MTTF			> 100 years

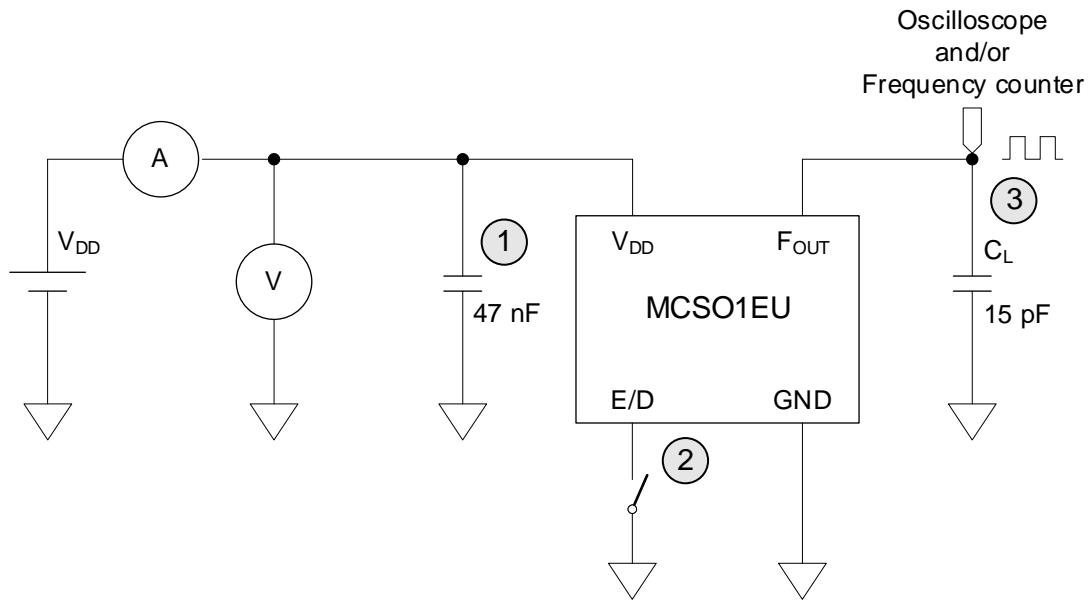
Terminal finish:



6. APPLICATION INFORMATION

6.1. TEST CIRCUIT

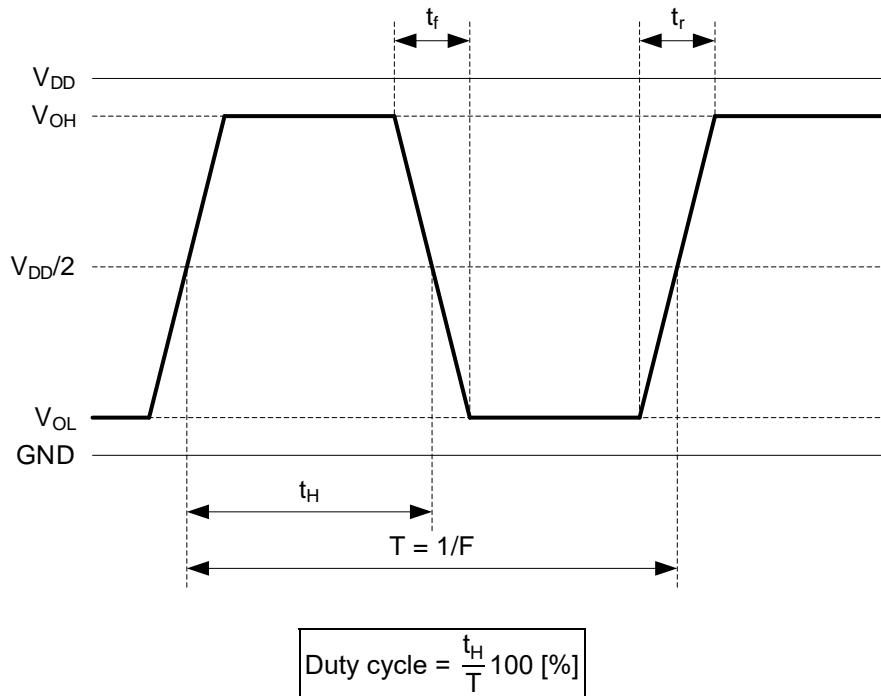
Test circuit HCMOS:



- (1) A 47 nF decoupling capacitor is recommended close to the device.
- (2) If E/D is HIGH or floating (V_{IH} or open), the F_{OUT} pin is in output mode. When E/D is tied to Ground (V_{IL}), the F_{OUT} pin is disabled (Hi-Z).
No E/D function before V_{DD} is set.
- (3) C_L includes probe and fixture capacitance.

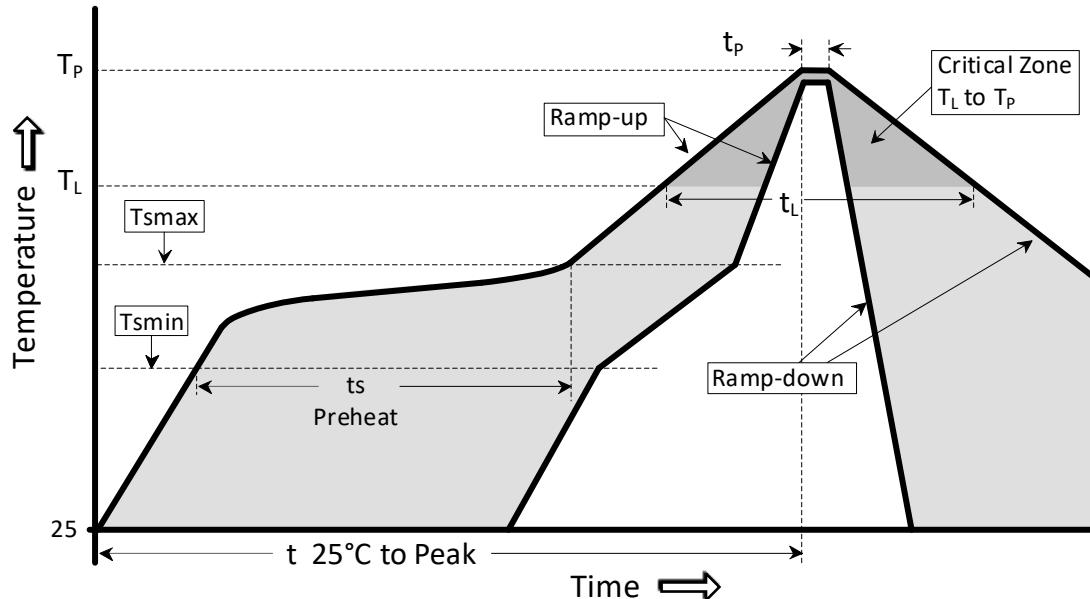
6.2. OUTPUT WAVEFORM

Output waveform HCMOS:



6.3. SOLDERING INFORMATION

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"



Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	$(T_{S\max} \text{ to } T_P)$	3°C / second max	°C / s
Ramp down Rate	T_{cool}	6°C / second max	°C / s
Time 25°C to Peak Temperature	$T_{\text{to-peak}}$	8 minutes max	min
Preheat			
Temperature min	$T_{S\min}$	150	°C
Temperature max	$T_{S\max}$	200	°C
Time $T_{S\min}$ to $T_{S\max}$	t_S	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	T_L	217	°C
Time above liquidus	t_L	60 – 150	sec
Peak temperature			
Peak Temperature	T_P	260	°C
Time within 5°C of peak temperature	t_P	20 – 40	sec

6.4. HANDLING PRECAUTIONS FOR MODULES WITH EMBEDDED CRYSTALS

The built-in AT-cut crystal consists of pure Silicon Dioxide in crystalline form. The cavity inside the package is evacuated and hermetically sealed in order for the crystal blank to function undisturbed from air molecules, humidity and other influences.

Shock and vibration:

Keep the crystal / module from being exposed to **excessive mechanical shock and vibration**. Micro Crystal guarantees that the crystal / module will bear a mechanical shock of 10000 g / 0.3 ms.

The following special situations may generate either shock or vibration:

Multiple PCB panels - Usually at the end of the pick & place process the single PCBs are cut out with a router. These machines sometimes generate vibrations on the PCB that have a fundamental or harmonic frequency close to 32.768 kHz. This might cause breakage of crystal blanks due to resonance. Router speed should be adjusted to avoid resonant vibration.

Ultrasonic cleaning - Avoid cleaning processes using ultrasonic energy. These processes can damage the crystals due to the mechanical resonance frequencies of the crystal blank.

Overheating, rework high temperature exposure:

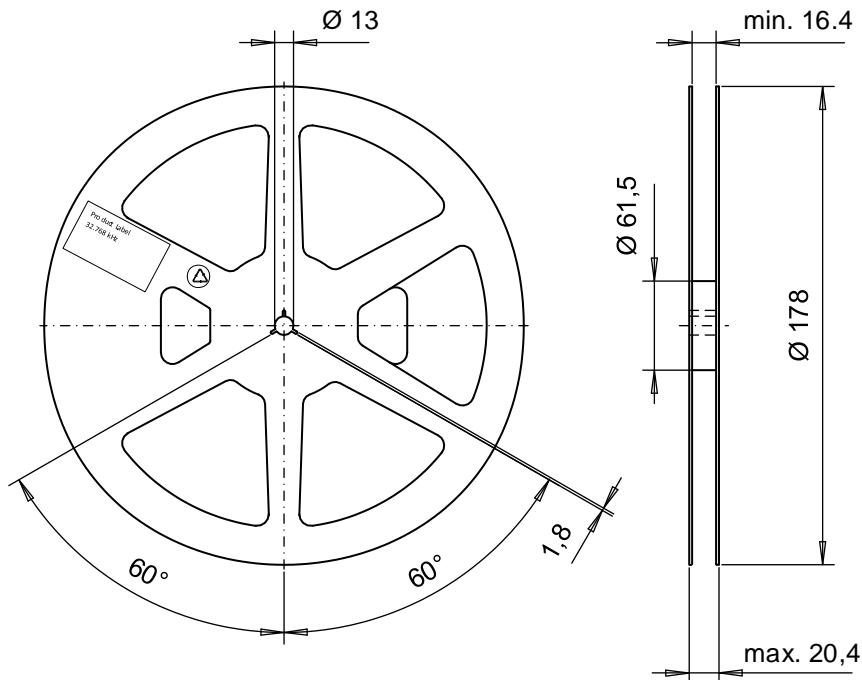
Avoid overheating the package. The package is sealed with a seal ring consisting of 80% Gold and 20% Tin. The eutectic melting temperature of this alloy is at 280°C. Heating the seal ring up to >280°C will cause melting of the metal seal which then, due to the vacuum, is sucked into the cavity forming an air duct. This happens when using hot-air-gun set at temperatures >280°C.

Use the following methods for rework:

- Use a hot-air-gun set at 270°C.
- Use 2 temperature controlled soldering irons, set at 270°C, with special-tips to contact all solder-joints from both sides of the package at the same time, remove part with tweezers when pad solder is liquid.

7. PACKING & SHIPPING INFORMATION

Reel: 7" = 178 mm

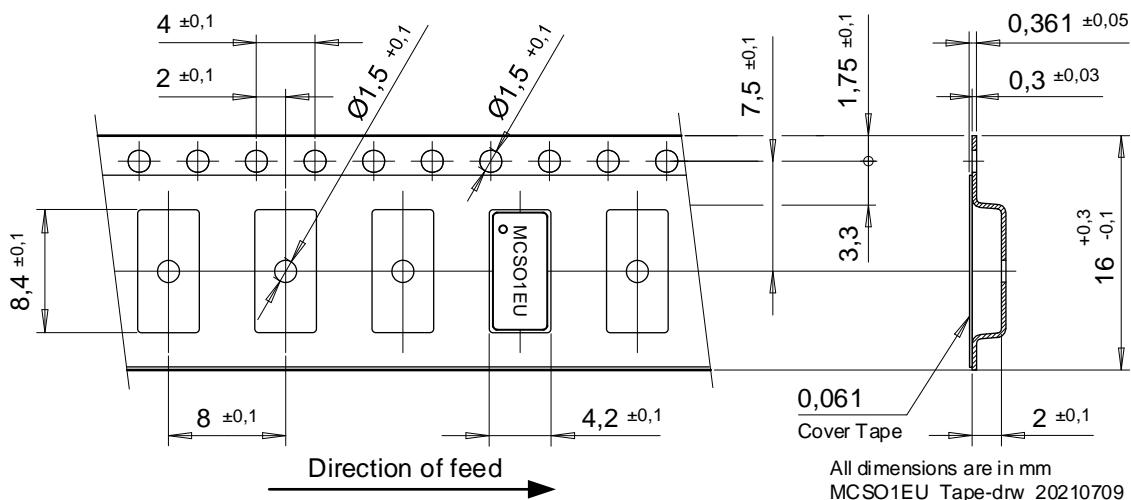


Carrier Tape:

Material: Polystyrene, conductive

Width: 16 mm

Tape Leader and Trailer: Minimum length 300 mm



All dimensions are in mm
MCSO1EU_Tape-drw_20210709

Cover Tape:

Tape: Polypropylene, 3M™ Universal Cover Tape (UCT)

Adhesive Type: Pressure sensitive, Synthetic Polymer

Thickness: 0.061 mm

Peel Method:

Medial section removal, both lateral stripes remain on carrier

8. COMPLIANCE INFORMATION

Micro Crystal confirms that the standard product Clock Oscillator MCSO1EU is compliant with "EU RoHS Directive" and "EU REACH Directives".

Please find the actual Certificate of Conformance for Environmental Regulations on our website:

[CoC_Environment_MCSO-Series.pdf](#)

9. DOCUMENT REVISION HISTORY

Date	Revision #	Revision Details
August 2021	1.0	First release

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August 2021

Micro Crystal AG
Muehlestrasse 14
CH-2540 Grenchen
Switzerland

Phone +41 32 655 82 82
sales@microcrystal.com
www.microcrystal.com

23/23

Rev. 1.0