



**RV-4162-C7** 

**Development Board** 

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Revision No.: 2



# **Development Board**

# RV-4162-C7

The RV-4162-C7 is soldered onto the Development Board. Every pin is either accessible at test pins 1 - 8 or at the test vias situated around the device.

The following passive components are already soldered on the Board:

C1	10 nF	Decoupling capacitor between $V_{SS}$ and $V_{DD}$
	000 0	

- R1
   330 Ω
   current limiting resistor for LED
- LED green Supply, current consumption of the LED has to be considered
- R2 10 k $\Omega$  Pull-up resistor SDA to V<sub>DD</sub>
- R3 10 k $\Omega$  Pull-up resistor INT to V<sub>DD</sub>
- R4 10 k $\Omega$  Pull-up resistor SCL to V<sub>DD</sub>

### **DEVELOPMENT BOARD**



# **Development Board**

# RV-4162-C7

#### SCHEMATICS



#### PINOUT RV-4162-C7



### PIN DESCRIPTION

Symbol	Pin #	Description	
SDA	1	Serial Data Input-Output pin; open-drain; requires pull-up resistor.	
CLKOUT	2	Clock Output pin; push-pull output; at power-up by default 32.768kHz	
V <sub>SS</sub>	3	Ground	
NC	4	Not Connected	
V <sub>DD</sub>	5	Positive supply voltage; recommend 10 nF decoupling capacitor close to device	
INT	6	Interrupt Output pin; open-drain; active LOW	
NC	7	Not Connected	
SCL	8	Serial Clock Input pin; requires pull-up resistor	

Datasheet and Application-Manual are available for download under: www.microcrystal.com